R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

INTERVIEW SUMMARY

Applicant's representative, John Ignatowski, spoke with Examiner Hau Nguyen and Supervisor Matthew Bella via telephone on March 23, 2004. The rejections under 35 U.S.C. §102 of the independent claims 1, 13 and 22 over Zhang, Porter and Moreton were discussed. An agreement was reached that none of the above references anticipate the claims and thus another search would be conducted and a new Office Action would be issued.

SUPPORT FOR THE SPECIFICATION AMENDMENTS

Support for the specification amendments may be found in U.S. Patent application serial number 09/960,578, incorporated by reference in its entirety into the present application, on page 5 lines 17-18, page 8 line 18 thru page 10 line 19 and page 11 line 3 thru page 13 line 19, as originally filed. Thus, no new matter has been added.

SUPPORT FOR THE DRAWING AMENDMENT

Support for the drawing amendment may be found in U.S. Patent application serial number 09/960,578, incorporated by

14

reference in its entirety into the present application, in FIG. 5 as originally filed. Thus, no new matter has been added.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1-3, 9-11, 13, 18-19, 21 and 22 under 35 U.S.C. §102(e) as being anticipated by Zhang et al. '711 (hereafter Zhang) is respectfully traversed and should be withdrawn.

The rejection of claims 1, 4, 5, 12-15 and 20 under 35 U.S.C. §102(e) as being anticipated by Porter '354 is respectfully traversed and should be withdrawn.

The rejection of claims 1, 7, 8, 13, 16 and 17 under 35 U.S.C. §102(e) as being anticipated by Moreton et al. '729 (hereafter Moreton) is respectfully traversed and should be withdrawn.

Zhang concerns a system and method for transporting a compressed video and data bit stream over a communication channel (Title). Porter concerns a method and apparatus for displaying multiple graphics images in a mixed video graphics display (Title). Moreton concerns a circuit to separate and combine color space component data of a video image (Title).

In contrast to the references, claim 1 provides (in part) a composite circuit configured to generate a combined output data stream in response to performing a spatial combination of a first

15

output data stream and a second output data stream. However, each of Zhang and Moreton are silent regarding spatial combinations of two data streams, as discussed during the Telephone Interview. Therefore, each of Zhang and Moreton do not disclose or suggest a composite circuit configured to generate a combined output data stream in response to performing a spatial combination of a first output data stream and a second output data stream as presently claimed. Claims 13 and 22 provide language similar to claim 1. As such, the claimed invention is fully patentable over each of Zhang and Morton and the rejections should be withdrawn.

Claim 1 further provides a first data modification circuit configured to generate a first output data stream in response to performing a first modification on at least one first image from a first input data stream. In contrast, each of Porter and Moreton are silent regarding a circuit modifying one or more images from a first data stream, as discussed during the Telephone Interview. Therefore, each of Porter and Moreton do not disclose or suggest a first data modification circuit configured to generate a first output data stream in response to performing a first modification on at least one first image from a first input data stream as presently claimed. Claim 13 provides language similar to claim 1. As such, the claimed invention is fully patentable over each of Porter and Moreton and the rejections should be withdrawn.

Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicant's representative at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit Account No. 12-2252.

Respectfully submitted,
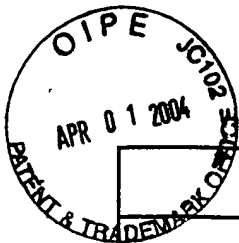
CHRISTOPHER P. MAIORANA, P.C.

Christopher P. Maiorana
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Dated: March 30, 2004

c/o Leo Peters
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1621 Barber Lane, M/S D-106 Legal
Milpitas, CA 95035

Docket No.: 00-335 / 1496.00154

17

Applicant Initiated Interview Request Form

Application No.: 09/960,771 First Named Applicant: _David N. Perber_

Examiner: _Nguyen, Hau H_   Art Unit: _2676_   Status of Application: _Final rejection_

Tentative Participants:

(1) _Examiner Nguyen_   (2) _John J. Ignatowski_

(3) _Supervisor Bella_   (4) _____

Proposed Date of Interview: _3/18/04_   Proposed Time: _1:30_ (AM/**PM**).

Type of Interview Requested:

(1) [X] Telephonic   (2) [ ] Personal   (3) [ ] Video Conference

Exhibit To Be Shown or Demonstrated: [ ] YES   [X] NO
If yes, provide brief description: _____

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Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) _Rej._	1-3, 9, 11, 13, 18, 19, 21, 22	Zhang et al.	[X]	[X]	[ ]
(2) _Rej._	1, 4, 5, 13-15	Porter	[X]	[X]	[ ]
(3) _Rej._	1, 7, 8, 13, 16, 17	Moreton et al.	[X]	[X]	[ ]
(4) _____	_____	_____	[ ]	[ ]	[ ]

[X] Continuation Sheet Attached

Brief Description of Arguments to be Presented:
See attached Continuation Sheets (two pages)

An interview was conducted on the above-identified application on _3/23/04_.

NOTE:
This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).
This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

(Applicant/Applicant's Representative Signature)   (Examiner/SPE Signature)

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Appl. No.: 09/960,771
Applicant: David N. Pether
Examiner: Nguyen, H.
Art Unit: 2676

Zhang
Claims 1, 13 and 22 *Zhang 520 ≠ an image*
1) Where does Zhang disclose a first input data stream having a first image? *— input into 504*
2) Where does Zhang disclose Modification Units 404-408 modifying the first image? *514 - 518*
3) Where does Zhang disclose a second input data stream? *one of outputs of parser 504*
4) Where does Zhang disclose Stream Bit Multiplexer 526 performing a spatial combination? *not in Zhang*

Claim 2 *Not discussed*
Where does Zhang disclose a block modify and move engine?

Claim 3 *Not discussed*
Where does Zhang disclose an image in the second input data stream?

Claims 9 and 18 *Not discussed*
Where does Zhang disclose scaling of an image?

Claims 11 and 19 *Not discussed*
Where does Zhang disclose that the Stream Bit Multiplexer 526 performs a bitwise logical
operation on the first output data stream and the second output data stream?

Claim 21 *Not discussed*
1) Where does Zhang disclose a memory configured to buffer a first data, a second data and a
third data?
2) Where does Zhang disclose a first expander to generate the first input data stream?
3) Where does Zhang disclose a second expander to generate the second input data stream?
4) Where does Zhang disclose the claimed third control signal?
5) Where does Zhang disclose a third expander to generate the control signal?

Porter
Claims 1 and 13
1) Where does Porter disclose a first input data stream having a first image? *Stream 16*
2) Where does Porter disclose the memory 10 modifying the first image? *not in Porter*

Claims 4 and 14 *Not discussed*
1) Where does Porter disclose the memory 10 converting an input format of a first input data
stream between a video data format and a graphics data format?
2) Where does Porter disclose the memory 10 converting an output format of a first output data
stream between a video data format and a graphics data format?

1

Appl. No.:   09/960,771

Claim 5   *Not discussed*
Where does Porter disclose placing video pictures in front of graphics data?

Claim 15   *Not discussed*
Where does Porter disclose placing graphics data in front of video images?

<u>Moreton</u>
Claims 1 and 13
1) Where does Moreton disclose modification of a first image in a first input data stream? *signal DIN*
2) What block(s) in FIG. 4 disclose the claimed first data modification circuit?
2) Where does Moreton disclose a second output data stream? *not in Moreton*
3) Where does Moreton disclose the multiplexer of FIG. 4 performing a spatial combination? *not in Moreton*

Claims 7 and 16   *Not discussed*
Where does Moreton disclose that the blocks in FIG. 4 (similar to the first data modification circuit) interleave color components in the first image?

Claims 8 and 17   *Not discussed*
Where does Moreton disclose that the blocks in FIG. 4 (similar to the first data modification circuit) perform separation of color components in the first image?

Examiner will conduct a new search then issue a new Office Action.

2